

Code.No: RR310201

RR

SET-1

III B.TECH – I SEM EXAMINATIONS, NOVEMBER - 2010
COMPUTER ORGANIZATION
(COMMON TO EEE, EIE, ECE, ETM, ICE)

Time: 3hours**Max.Marks:80**

Answer any FIVE questions
All questions carry equal marks

- - -

- 1.a) Compare dedicated and multiplexed bus lines.
b) Discuss various bus arbitration techniques. [8+8]
2. Convert the following decimal numbers to base three and base five.
i) 75
ii) 13.245
iii) 25.21 [16]
3. Explain common addressing modes with examples. [16]
4. Explain instruction cycle in detail. [16]
5. Explain different techniques of cache mapping functions and specify merits and demerits of each. [16]
- 6.a) Explain associative memory organization.
b) What is demand paging? Explain merits and demerits of it. [8+8]
- 7.a) Give the general-description of on I/O module structure.
b) Discuss about possible DMA configurations.
c) What is interrupt service Routine? Explain. [16]
- 8.a) Classify and explain different multiprocessors.
b) How branch instruction is handled in pipelined processors? Explain. [8+8]

--ooOoo--

Code.No: RR310201

RR

SET-2

III B.TECH – I SEM EXAMINATIONS, NOVEMBER - 2010
COMPUTER ORGANIZATION
(COMMON TO EEE, EIE, ECE, ETM, ICE)

Time: 3hours**Max.Marks:80**

Answer any FIVE questions
All questions carry equal marks

- - -

1. Explain common addressing modes with examples. [16]
2. Explain instruction cycle in detail. [16]
3. Explain different techniques of cache mapping functions and specify merits and demerits of each. [16]
- 4.a) Explain associative memory organization.
b) What is demand paging? Explain merits and demerits of it. [8+8]
- 5.a) Give the general-description of on I/O module structure.
b) Discuss about possible DMA configurations.
c) What is interrupt service Routine? Explain. [16]
- 6.a) Classify and explain different multiprocessors.
b) How branch instruction is handled in pipelined processors? Explain. [8+8]
- 7.a) Compare dedicated and multiplexed bus lines.
b) Discuss various bus arbitration techniques. [8+8]
8. Convert the following decimal numbers to base three and base five.
i) 75
ii) 13.245
iii) 25.21 [16]

--ooOoo--

Code.No: RR310201

RR

SET-3

III B.TECH – I SEM EXAMINATIONS, NOVEMBER - 2010
COMPUTER ORGANIZATION
(COMMON TO EEE, EIE, ECE, ETM, ICE)

Time: 3hours**Max.Marks:80**

Answer any FIVE questions
All questions carry equal marks

- - -

1. Explain different techniques of cache mapping functions and specify merits and demerits of each. [16]
- 2.a) Explain associative memory organization.
b) What is demand paging? Explain merits and demerits of it. [8+8]
- 3.a) Give the general-description of on I/O module structure.
b) Discuss about possible DMA configurations.
c) What is interrupt service Routine? Explain. [16]
- 4.a) Classify and explain different multiprocessors.
b) How branch instruction is handled in pipelined processors? Explain. [8+8]
- 5.a) Compare dedicated and multiplexed bus lines.
b) Discuss various bus arbitration techniques. [8+8]
6. Convert the following decimal numbers to base three and base five.
i) 75
ii) 13.245
iii) 25.21 [16]
7. Explain common addressing modes with examples. [16]
8. Explain instruction cycle in detail. [16]

--ooOoo--

Code.No: RR310201

RR

SET-4

III B.TECH – I SEM EXAMINATIONS, NOVEMBER - 2010
COMPUTER ORGANIZATION
(COMMON TO EEE, EIE, ECE, ETM, ICE)

Time: 3hours**Max.Marks:80**

Answer any FIVE questions
All questions carry equal marks

- 1.a) Give the general-description of on I/O module structure.
b) Discuss about possible DMA configurations.
c) What is interrupt service Routine? Explain. [16]
- 2.a) Classify and explain different multiprocessors.
b) How branch instruction is handled in pipelined processors? Explain. [8+8]
- 3.a) Compare dedicated and multiplexed bus lines.
b) Discuss various bus arbitration techniques. [8+8]
4. Convert the following decimal numbers to base three and base five.
i) 75
ii) 13.245
iii) 25.21 [16]
5. Explain common addressing modes with examples. [16]
6. Explain instruction cycle in detail. [16]
7. Explain different techniques of cache mapping functions and specify merits and demerits of each. [16]
- 8.a) Explain associative memory organization.
b) What is demand paging? Explain merits and demerits of it. [8+8]

--ooOoo--